

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): KAJIHARA, et al.  
Serial No.: 09/328,910  
Filed: June 9, 1999  
For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE USING THE SAME AND METHOD OF AND  
PROCESS FOR FABRICATING THE TWO  
Group: 2815  
Examiner: Jasmine Jhihan B Clark  
Conf. No.: 7455

**STATEMENT OF SUBSTANCE OF INTERVIEW**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 12, 2010

Sir:

The following statement of the substance of the telephonic interview conducted between the SPE and the undersigned on September 17, 2010 is being submitted in reply to the requirement in the Interview Summary.

The Examiner's statement of the substance of the interview in the Interview Summary is complete and correct.

Respectfully submitted,

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